

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hitoshi TSUNO

Title: SEMICONDUCTOR DEVICE INCLUDING MOS FIELD EFFECT
TRANSISTOR HAVING OFFSET SPACERS OR GATE SIDEWALL FILMS
ON EITHER SIDE OF GATE ELECTRODE AND METHOD OF
MANUFACTURING THE SAME

Appl. No.: Unassigned

Filing Date: 10/02/2003

Examiner: Unassigned

Art Unit: Unassigned

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.56

Mail Stop PATENT APPLICATION
Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of documents known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56. A copy of each listed document, except as noted below, is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The USPTO has waived the requirement under 37 CFR 1.98(a)(2)(i) to submit copies of U.S. patents and U.S. patent application publications when citing and submitting an Information Disclosure Statements in a patent application filed after June 30, 2003 and in an international application that has entered the national stage under 37 USC §371 after June 30, 2003. Accordingly, copies of these types of documents are not being supplied in connection with this application. Reference is being made to Pre-OG Notice from Office of Patent Legal Administration dated July 25, 2003, *Information Disclosure Statements May Be Filed*

Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

TIMING OF THE DISCLOSURE

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the filing date of the application.

RELEVANCE OF EACH DOCUMENT

Document A4 discloses a CMOS device which includes a first conductive type channel MOSFET having first side-wall spacers on side surfaces and having a source and drain region of an LDD structure, and a second conductive type channel MOSFET having second side-wall spacers on side surfaces and having a source and drain region of a single drain structure, wherein a width of the first side-wall spacers is larger than that of the second side-wall spacers. Documents A4 and A7 claim priority based on the same first filed application.

Document A5 discloses a semiconductor device manufacturing method and a semiconductor device. Documents A1 and A5 claim priority based on the same first-filed application.

Document A6 discloses a semiconductor device having different sidewall widths and different source/drain depths for NMOS and PMOS structures. Documents A3 and A6 claim priority based on the same first-filed application.

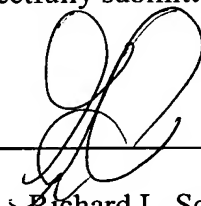
English translation of the foreign-language documents is not readily available. However, the absence of such translation does not relieve the PTO from its duty to consider the submitted foreign language documents (37 CFR §1.98 and MPEP §609).

Applicant respectfully requests that any listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

By



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Date October 2, 2003

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT Date Submitted: October 2, 2003 <i>(use as many sheets as necessary)</i>				Complete if Known		
				Application Number		Unassigned
				Filing Date		10/02/2003
				First Named Inventor		Hitoshi TSUNO
				Group Art Unit		Unassigned
				Examiner Name		Unassigned
Sheet 1 of 1				Attorney Docket Number		016907-1577

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	A1	6,333,540	B1	SHIOZAWA ET AL.	12-25-2001	
	A2	6,387,743	B1	SHIOZAWA ET AL.	05-14-2002	
	A3	5,994,743		MASUOKA	11-30-1999	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	A4	JP	11-220038	A	LSI LOGIC CORP.	08-10-1999		Abstract
	A5	JP	2001-308318	A	MITSUBISHI ELECTRIC CORP.	11-02-2001		Abstract
	A6	JP	10-223772	A	NIPPON ELECTRIC CO.	08-21-1998		Abstract
	A7	EP	0 932 197	A1	LSI LOGIC CORPORATION	07-28-1999		

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.		T ⁶

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, PO Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450.